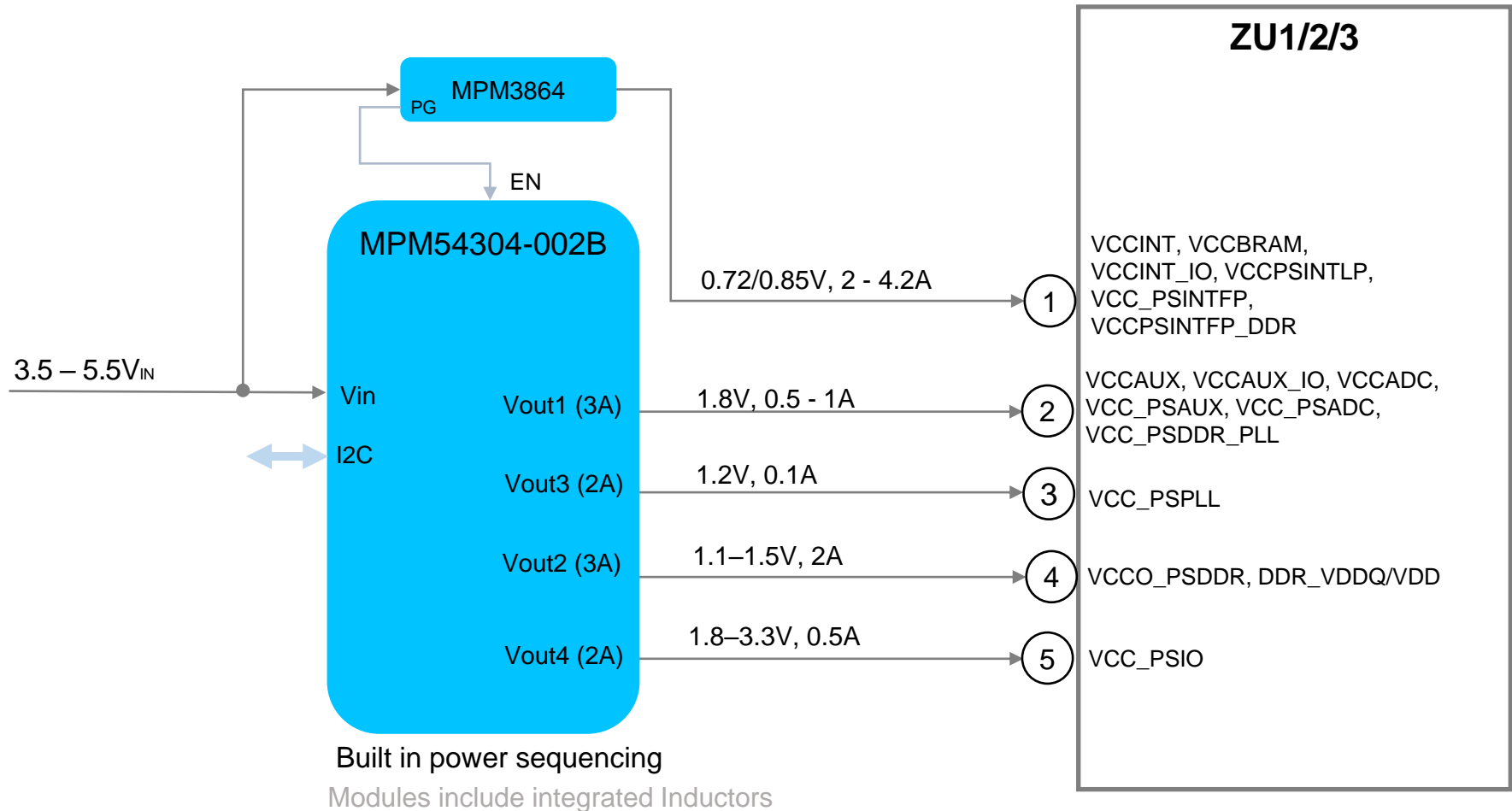


Zynq UltraScale+ Test Report

Size Optimized: ZU1/2/3

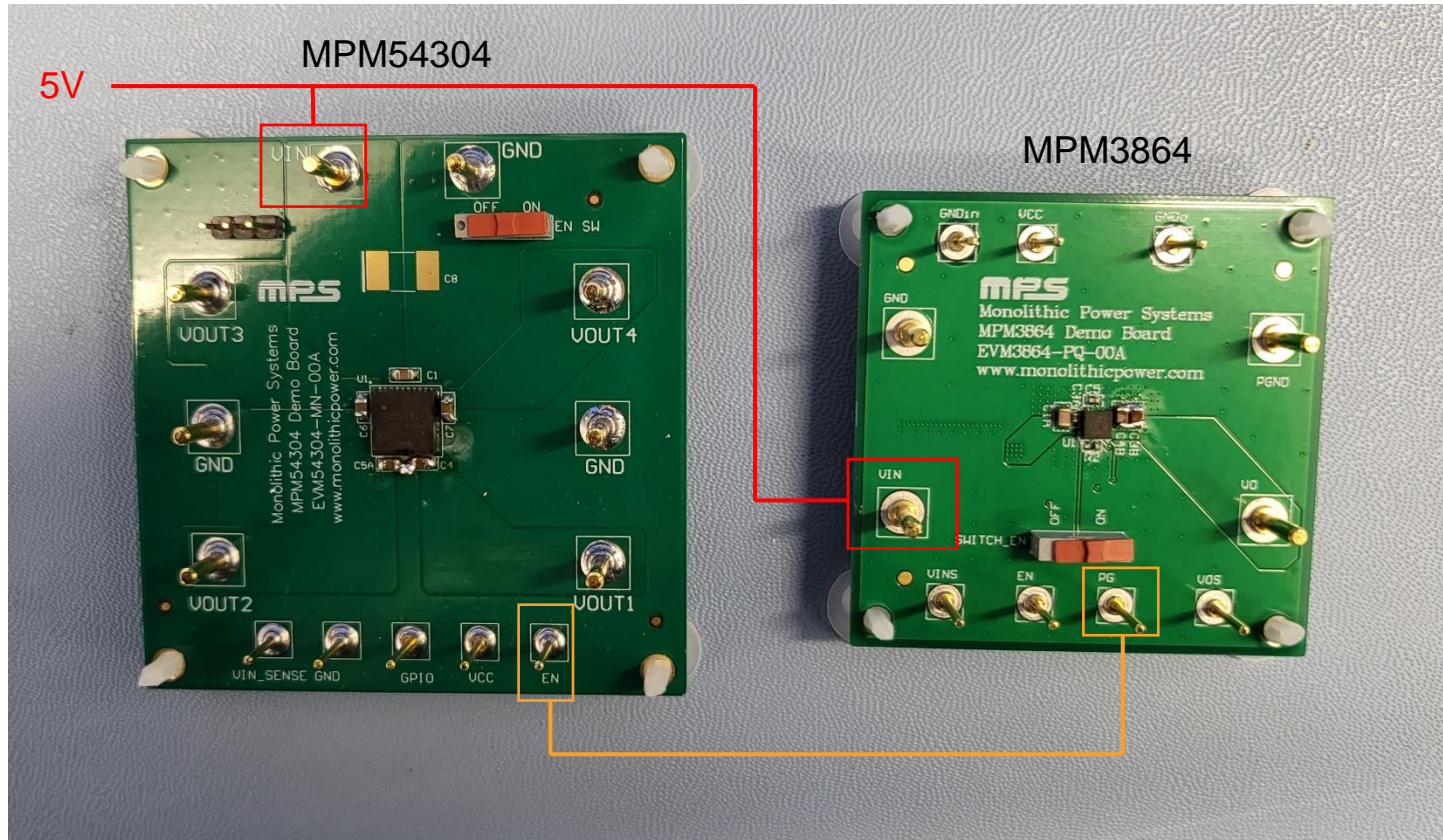
Updated
1/25/2024

ZU1/2/3 Minimum Rails: Size Optimized (PMIC)



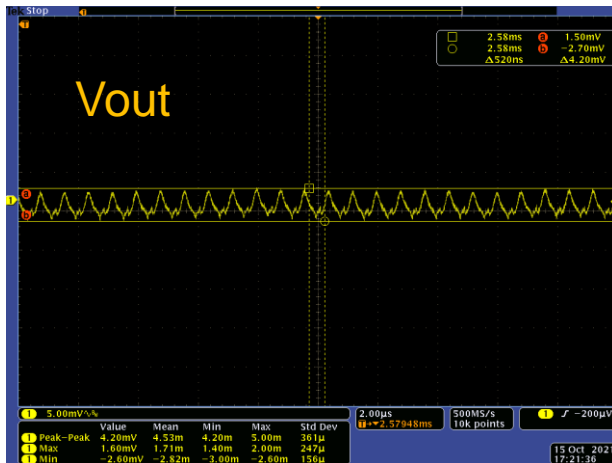
Power on sequence

ZU1/2/3 Minimum Rails (MP54304 PMIC Module) EVB Connection

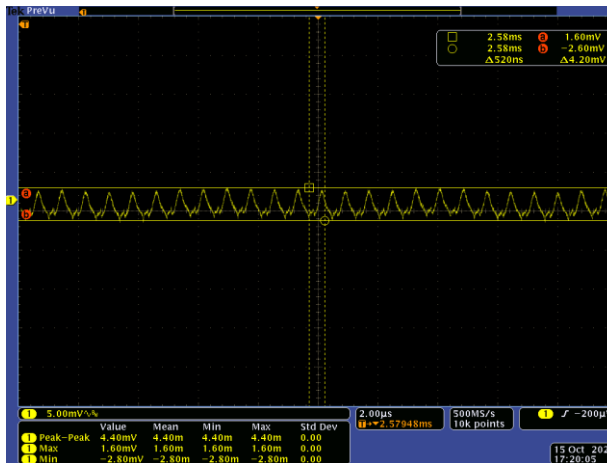


ZU1/2/3 Minimum Rails VCCINT Ripple (MPM3864 Module)

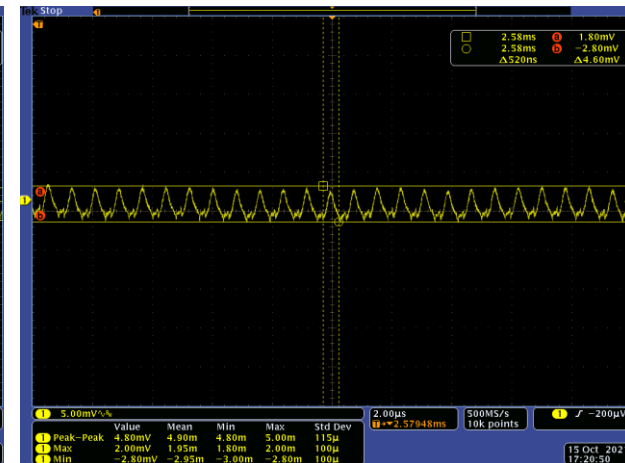
Standby



Half Load



Max Load



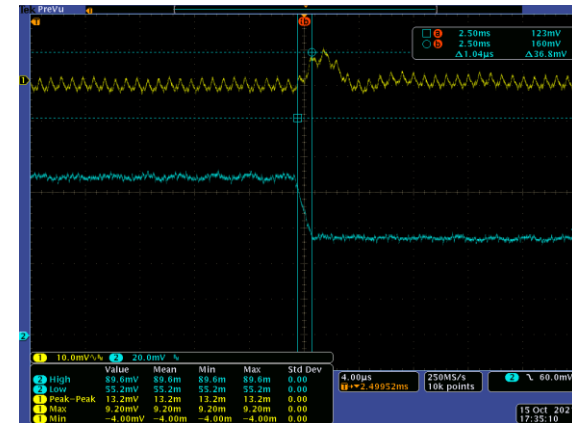
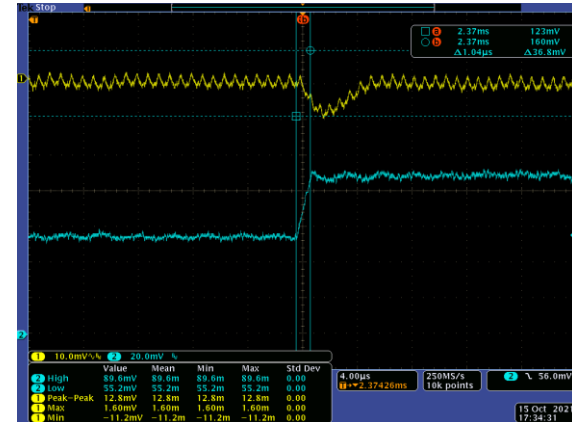
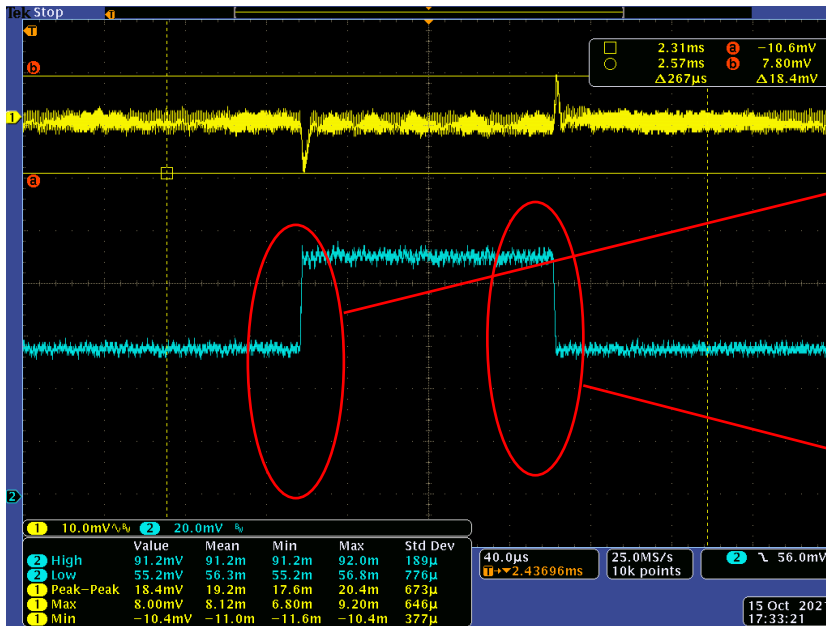
- ❑ 4.2 mV peak-peak ripple at Standby
- ❑ 4.4 mV peak-peak ripple at Half load
- ❑ 4.8 mV peak-peak ripple at Max load

ZU1/2/3 Minimum Rails VCCINT Transient (MPM3864 Module)

Step Load 2.1A → 3.15A → 2.1A, 10A/us

Vout

Io



Vout ripple -1.2% (-10.4mV) to +0.9% (8.0mV) with load transient

AU10P/AU15P/AU20P/AU25P (MPM54304 PMIC Module)

	Vo	Ripple No load	Ripple Half load	Ripple Full load	Pwr ON Seq
MPM3864	0.85V	4.2mVpp	4.4mVpp	4.8mVpp	1 (0ms)
MPM54304-Buck1	1.8V	15.2mVpp	15.2mVpp	23mVpp	2 (1ms)
MPM54304-Buck3	1.2V	9.6mVpp	10mVpp	10mVpp	3 (2mS)
MPM54304-Buck2	1.5V	10.8mVpp	11.6mVpp	12mVpp	4 (3mS)
MPM54304-Buck4	3.3V	20mVpp	21mVpp	21.6mVpp	5 (4mS)