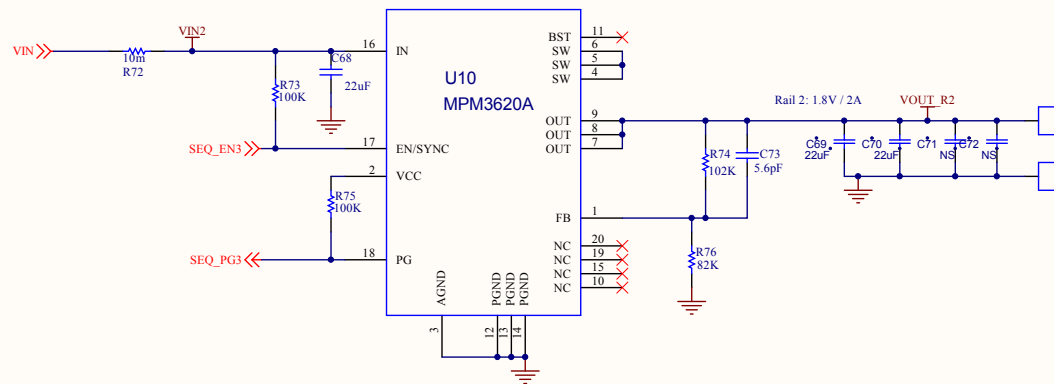
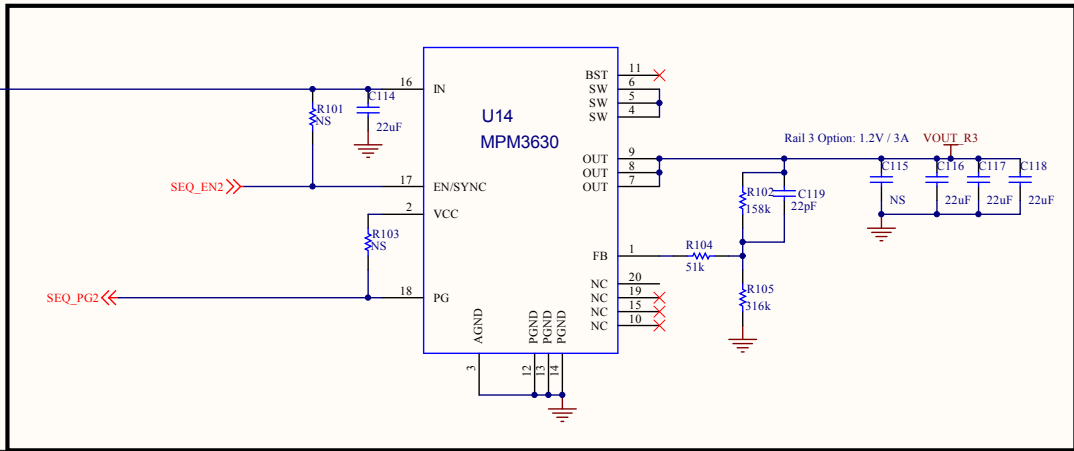
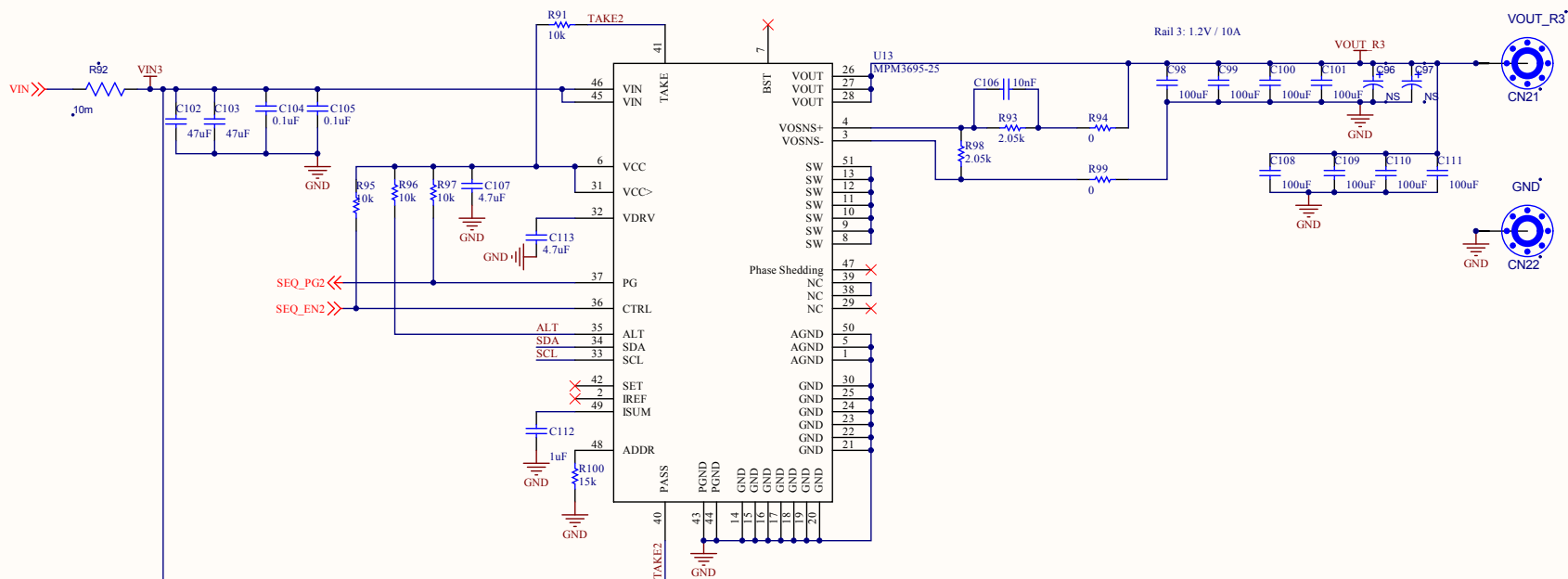


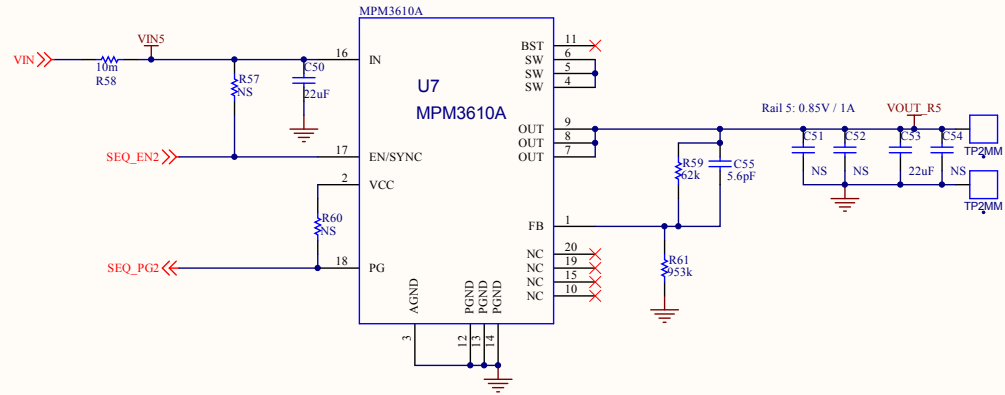
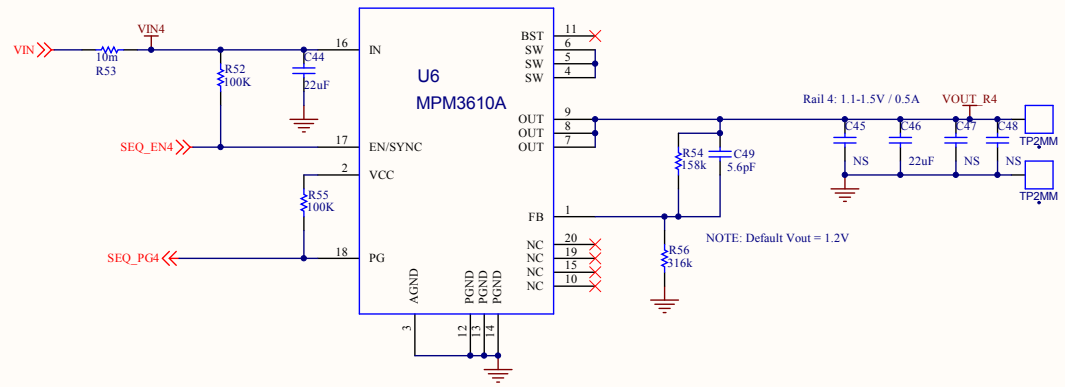
Title			EVREF0101		
Size	Number			Revision	0.1
A3					
Date:	9/2/2017	Sheet	1 of 8		
File:	C:\Users\JP1\Rail1.SchDoc	Drawn By:	Christian Cross		



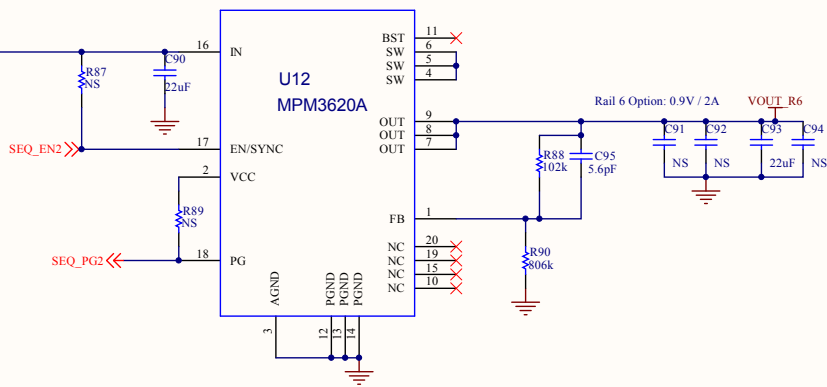
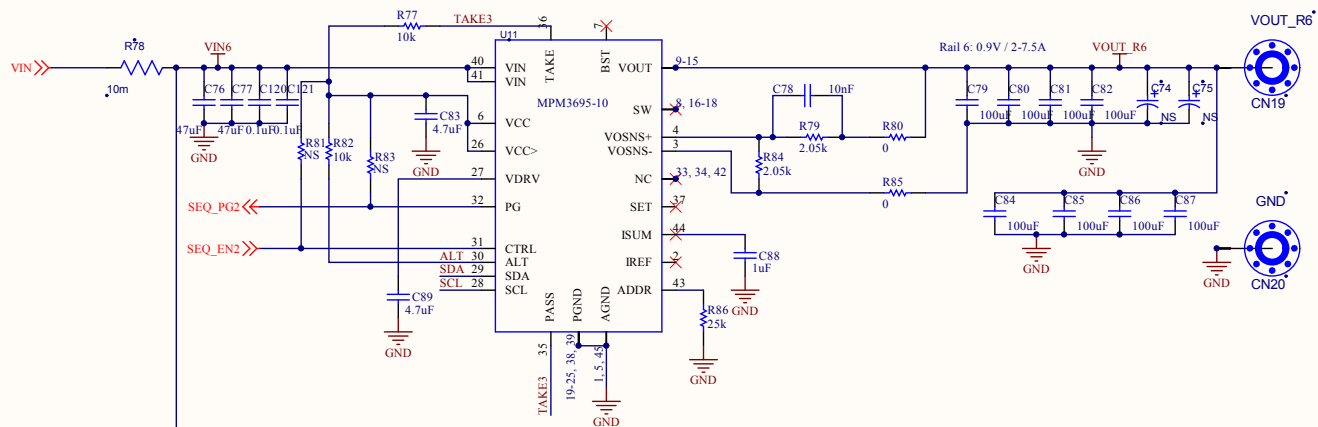
Title		
EVREF0101		
Size	Number	Revision
A		0.1
Date:	9/2/2017	Sheet 2 of 8
File:	C:\Users\JP2_Rail 2\SchDoc	Drawn By: Christian Cross



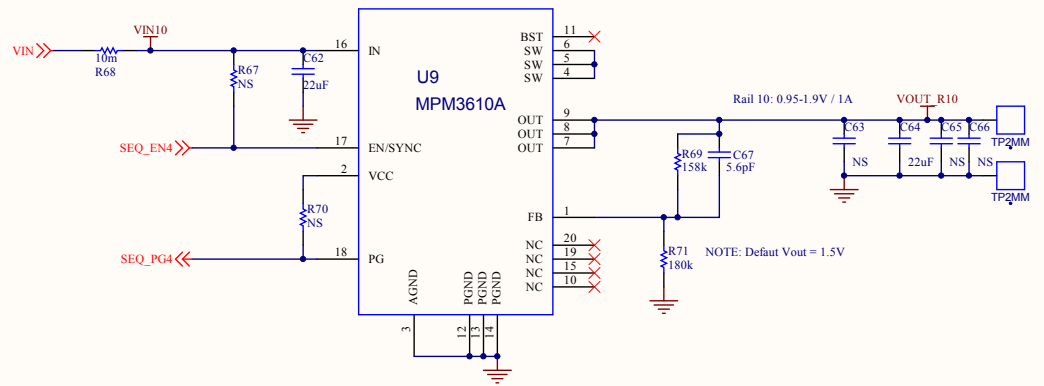
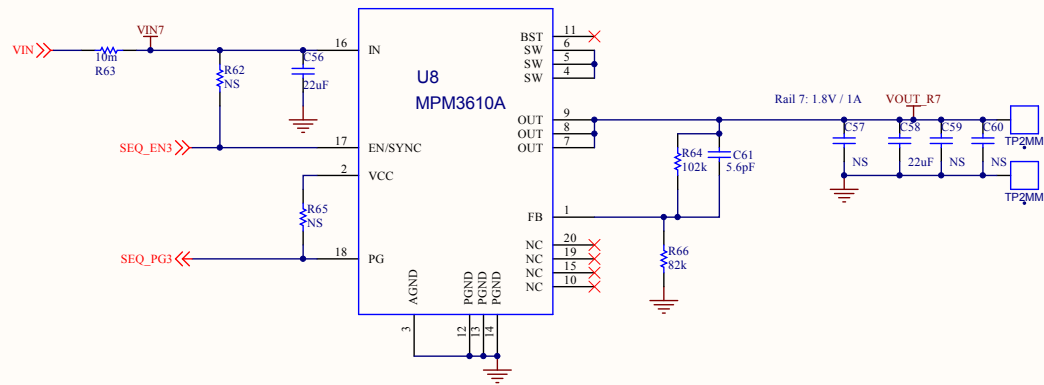
Title EVREF0101		
Size A	Number	Revision 0.1
Date: 9/2/2017	Sheet 3 of 8	8
File: C:\Users\JP3\Rail 3.SchDoc	Drawn By: Christian Cross	



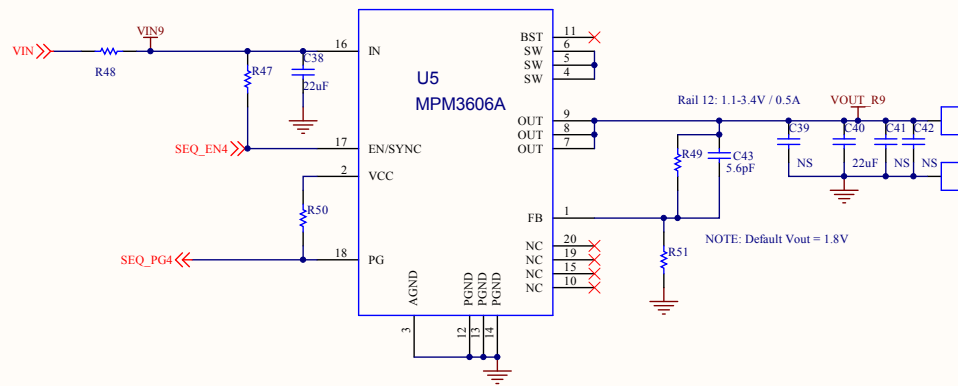
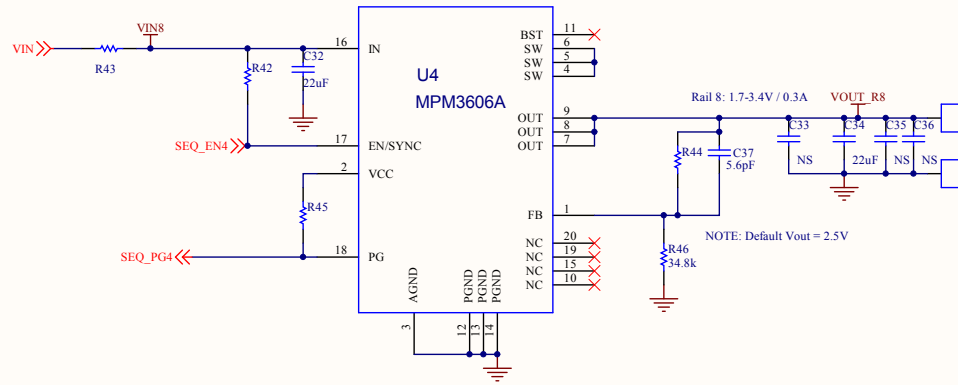
Title		
EVREF0101		
Size	Number	Revision
A		0.1
Date:	9/2/2017	Sheet 4of 8
File:	C:\Users\JP4\Rail 4 and Rail 5.SchDoc\Drawn By: Christian Cross	



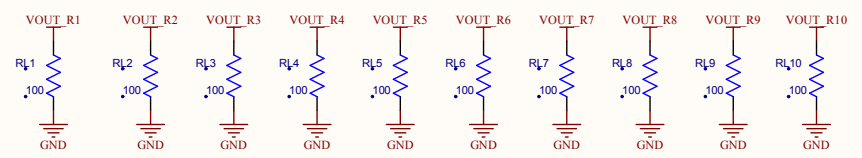
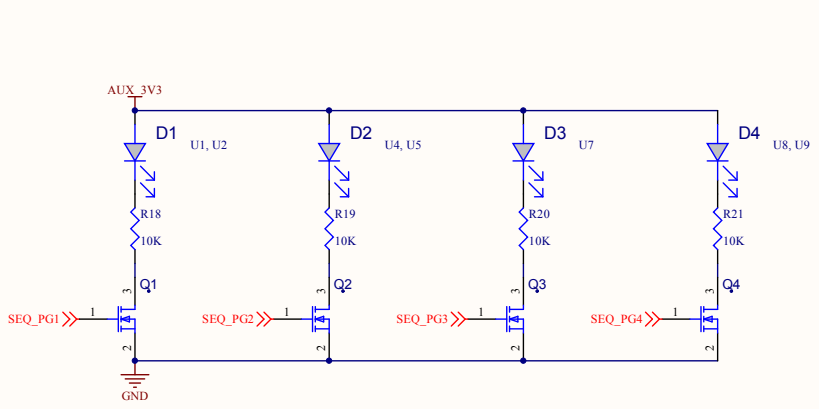
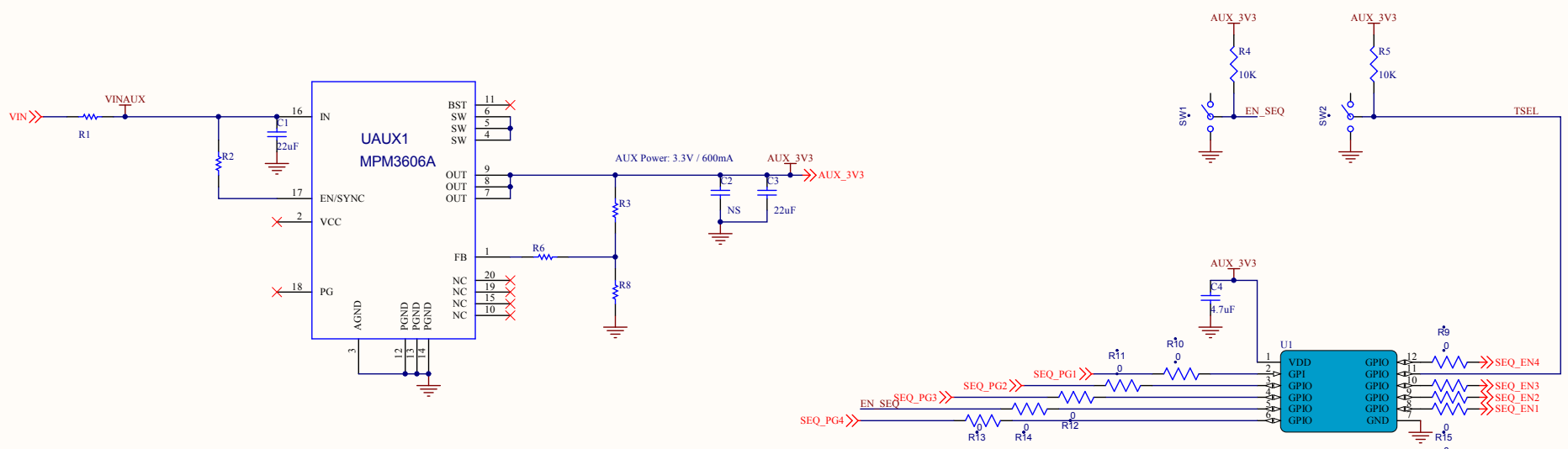
Title		
EVREF0101		
Size	Number	Revision
A		0.1
Date:	9/2/2017	Sheet 5 of 8
File:	C:\Users\JP5_Rail 6.SchDoc	Drawn By: Christian Cross



Title EVREF0101		
Size A	Number	Revision 0.1
Date: 9/2/2017	Sheet 6of 8	8
File: C:\Users\JP6\Rail 7 and Rail 10.Sch Drawn By: Christian Cross		



Title EVREF0101		
Size A	Number	Revision 0.1
Date: 9/2/2017	Sheet 7 of 8	
File: C:\Users\JP7\Rail 8 and Rail 9.SchDoc	Drawn By: Christian Cross	



Title		
EVREF0101		
Size	Number	Revision
A		0.1
Date:	9/2/2017	Sheet 8 of 8
File:	C:\Users\JP8\Aux Circuit.SchDoc	Drawn By: Christian Cross