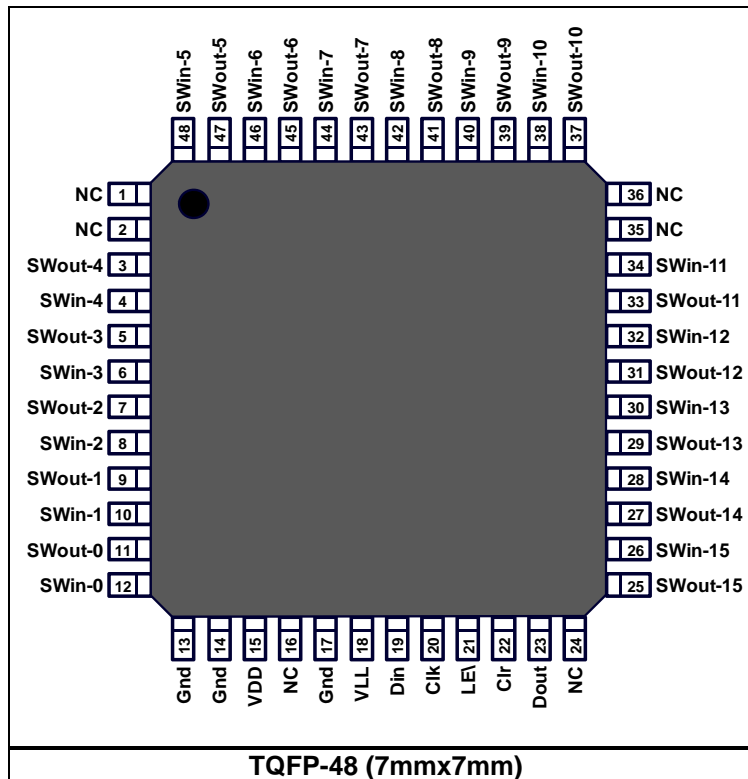


ORDERING INFORMATION

Part Number*	Package	Top Marking
MP4816GFP	TQFP-48 (7mmx7mm)	TBD

* For Tape & Reel, add suffix –Z (e.g. MP4816GFP–Z).

PACKAGE REFERENCE



ABSOLUTE MAXIMUM RATINGS ⁽¹⁾

V_{LL} , Logic supply –0.5V to +6.6V
 V_{DD} , Translator supply –0.5V to +11V
 V_{SIG} , Analog signal range 0V to $\pm 105V$
 Junction Temperature 150°C
 Lead Temperature 260°C
 Continuous Power Dissipation, $T_A=25^\circ C$ ⁽²⁾ 1.0W
 Storage temperature –55°C to 150°C

Recommended Operating Conditions ⁽³⁾

Logic supply voltage, V_{LL} 2.7V to 5.5V
 Translator supply voltage, V_{DD} 9V to 10V
 Analog signal range, V_{SIG} 0 to $\pm 90V$
 Junction temperature, T_J –25°C to +125°C

Thermal Resistance ⁽⁴⁾

TQFP-48 (7mmx7mm)	θ_{JA}	θ_{JC}
	68	15 °C/W

Notes:

- Exceeding these ratings may damage the device.
- The maximum allowable power dissipation is a function of the maximum junction temperature T_J (MAX), the junction-to-ambient thermal resistance θ_{JA} , and the ambient temperature T_A . The maximum allowable continuous power dissipation at any ambient temperature is calculated by P_D (MAX) = $(T_J$ (MAX)– T_A)/ θ_{JA} . Exceeding the maximum allowable power dissipation will cause excessive die temperature resulting in permanent damage.
- The device is not guaranteed to function outside of its operating conditions.
- Measured on JESD51-7, 4-layer PCB.